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- (71) Applicant (for all designated States except US): GREAT WALL SEMICONDUCTOR CORPORATION [US/US]; P.O.Box 24619, Southern Avenue, Tempe, AZ 85285-4619 (US).
- (72) Inventors: SHEN, Zheng; 3719 Savannah Loop, Oviedo, FL 32765 (US). OKADA, David, N.; 7855 South River Parkway, Suite 122, Tempe, AZ 85284 (US).
- (74) Agents: SAMUEL, Richard, I. et al.; Goodwin Procter LLP, 103 Eisenhower Parkway, Roseland, NJ 07068 (US).
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(54) Title: MONOLITHIC POWER SEMICONDUCTOR STRUCTURES

(57) Abstract: Provided herein are exemplary embodiments of monolithic semiconductor structures having at least two lateral constructed semiconductor devices (102, 104) combined on a single semiconductor substrate (106).